



Logic Non-Volatile Memory : The NVM Solutions from eMemory (International Series on Advances in Solid State Electronics)

Charles Ching-Hsiang Hsu, Yuan-Tai Lin, Evans Ching-Sung Yang, Rick Shih-Jye Shen

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Would you like to add the capabilities of the Non-Volatile Memory (NVM) as a storage element in your silicon integrated logic circuits, and as a trimming sector in your high voltage driver and other silicon integrated analog circuits? Would you like to learn how to embed the NVM into your silicon integrated circuit products to improve their performance?

This book is written to help you.

It provides comprehensive instructions on fabricating the NVM using the same processes you are using to fabricate your logic integrated circuits. We at our eMemory company call this technology the embedded Logic NVM. Because embedded Logic NVM has simple fabrication processes, it has replaced the conventional NVM in many traditional and new applications, including LCD driver, LED driver, MEMS controller, touch panel controller, power management unit, ambient and motion sensor controller, micro controller unit (MCU), security ID setting tag, RFID, NFC, PC camera controller, keyboard controller, and mouse controller. The recent explosive growth of the Logic NVM indicates that it will soon dominate all NVM applications. The embedded Logic NVM was invented and has been implemented in users' applications by the 200+ employees of our eMemory company, who are also the authors and author-assistants of this book.

This book covers the following Logic NVM products: One Time Programmable (OTP) memory, Multiple Times Programmable (MTP) memory, Flash memory, and Electrical Erasable and Programmable Read Only Memory (EEPROM). The fundamentals of the NVM are described in this book, which include: the physics and operations of the memory transistors, the basic building block of the memory cells and the access circuits.

All of these products have been used continuously by the industry worldwide. In-depth readers can attain expert proficiency in the implementation of the embedded Logic NVM technology in their products.

Readership: Advanced undergraduate students and graduate students in electrical, electronics, computer engineering, and physics departments; university researchers and teachers interested in NVM; device and process engineers; circuit designers; product engineers; reliability engineers; and engineering managers.

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